



Electronic Filing System (EFS) Data
Electronic Patent Application Submission
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4-26-04

EFS ID: 57722
Application ID: 10063237
Title of Invention: Method and Apparatus for
Ethernet Prioritized Device Clock
Synchronization
First Named Inventor: Francois Jammes
Domestic/Foreign Application: Domestic Application
Filing Date: 2002-04-02
Effective Receipt Date: 2004-03-24
Submission Type: Information Disclosure
Statement
Filing Type:
Confirmation number: 6902
Attorney Docket Number: SAA-79 (402P272)



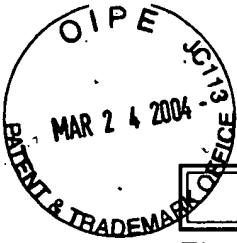
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Total Fees Authorized:

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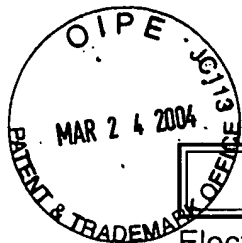


TRANSMITTAL

Electronic Version v1.1
Stylesheet Version v1.1.0

#6

Title of Invention	Method and Apparatus for Ethernet Prioritized Device Clock Synchronization									
<div>Application Number: 10/063237</div> <div>Date: 2002-04-02</div> <div>First Named Applicant: Mr. Francois Jammes</div> <div>Confirmation Number: 6902</div> <div>Attorney Docket Number: SAA-79 (402P272)</div> <div>RECEIVED MAR 26 2004 Technology Center 2600</div>										
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<table border="1"><thead><tr><th>Submitted by:</th><th>Elec. Sign.</th><th>Sign. Capacity</th></tr></thead><tbody><tr><td>Mr. James P. Muraff Registered Number: 39,785</td><td>James P. Muraff</td><td>Attorney</td></tr></tbody></table>			Submitted by:	Elec. Sign.	Sign. Capacity	Mr. James P. Muraff Registered Number: 39,785	James P. Muraff	Attorney		
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Mr. James P. Muraff Registered Number: 39,785	James P. Muraff	Attorney								
<table><tr><td>Documents being submitted</td><td>Files</td></tr><tr><td>us-ids</td><td>402P272SupplDS2-usidst.xml</td></tr><tr><td></td><td>us-ids.dtd</td></tr><tr><td></td><td>us-ids.xsl</td></tr></table>			Documents being submitted	Files	us-ids	402P272SupplDS2-usidst.xml		us-ids.dtd		us-ids.xsl
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us-ids	402P272SupplDS2-usidst.xml									
	us-ids.dtd									
	us-ids.xsl									
<div>Comments</div> <div>SAA-79 (402P272) Supplemental IDS No. 2</div>										



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
Stylesheet Version v18.0

#6

Title of
Invention

Method and Apparatus for Ethernet Prioritized Device Clock
Synchronization

Application Number: 10/063237
Confirmation Number: 6902
First Named Applicant: Francois Jammes
Attorney Docket Number: SAA-79 (402P272)
Art Unit: 2661
Search string: (4669040 or 4918690 or 5612982 or 5684375



or 5790977 or 5801689 or 5990884 or 6134522
or 6151625 or 6201996 or 6233626 or 6282454
or 6311101 or 6321272 or 6327511 or 6370550
or 6424872 or 6434157 or 6453210 or 6466995
or 6484061 or 6505341 or 6370569 or
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US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	4669040	1987-05-26	Pettit et al.			
	2	4918690	1990-04-17	Markkula Jr. et al.			
	3	5612982	1992-11-10	Mentler			
	4	5684375	1997-11-04	Chaffee et al.			
	5	5790977	1998-08-04	Ezekiel			
	6	5801689	1998-09-01	Huntsman			
	7	5990884	1999-11-23	Douma et al.			
	8	6134522	2000-10-17	Fritz et al.			
	9	6151625	2000-11-21	Swales et al.			
	10	6201996	2001-03-13	Crater et al.			
	11	6233626	2001-05-15	Swales et al.			
	12	6282454	2001-08-28	Papadopoulos et al.			
	13	6311101	2001-10-30	Kastner			
	14	6321272	2001-11-20	Swales			

	15	6327511	2001-12-04	Naismith et al.
	16	6370550	2002-04-09	Douma et al.
	17	6424872	2002-07-23	Glanzer et al.
	18	6434157	2002-08-13	Dube et al.
	19	6453210	2002-09-17	Belotserkovsky
	20	6466995	2002-10-15	Swales et al.
	21	6484061	2002-11-19	Papadopoulos et al.
	22	6505341	2003-01-07	Harris et al.
	23	6370569	2002-04-09	Austin

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20020176441	2002-11-28	Swales et al.			

Remarks

Note: Remarks are not for responding to an office action.

SAA-79 (402P272) Supplemental IDS No. 2

Signature

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Examiner Name	Date